

DECLARATION FOR UTILITY OR



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Joseph Wayne Forler

DESIGN							
PATENT APPLICATION COMPLETE IF KNOWN							
(37 CFR 1.6	Application Num	ber					
_	,	Filing Date	_				
	mitted after Initial	Group Art Unit					
Filing (37 C	g (surcharge CFR 1.16 (e)) ired)	Examiner Name					
		· .					
As a below named inventor, I hereby o							
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I believe I am the original, first and sole in names are listed below) of the subject m							
PROGRAM SIGNAL BLOC	CKING SYSTEM						
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the specification of which	(Title	of the Invention)					
is attached hereto OR							
was filed on (MM/DD/YYYY) December 18, 1997 as United States Application Number or PCT International							
Application Number PCT/US97/23838 and was amended on (MM/DD/YYYY) December 13, 1999 (if applicable).							
I hereby state that I have reviewed and ur amended by any amendment specifically		of the above identified specifi	cation, including the	daims, as			
I acknowledge the duty to disclose information		o patentability as defined in 3	37 CFR 1.56.	,			
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Prior Foreign Application Number(s)	Country	Foreign Filing Date (MM/DD/YYYY)	Priority Not Claimed	Certified Copy Attached?			
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DECLARATION—Utility or Design Patent Application

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PCT/US97/23838			December 18, 1997									
Additional L	J.S. or PCT	international applica	tion number	rs are listed on	a supplem	ental or	iority data	sheet PTO/SR/	02B attact	hed heret	in	
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Given Name (first and middle [if any]) Family Name or Surname												
JOSEPH WAYNE FORLER												
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Additional inventors are being named on the supplemental Additional Inventor(s) sheet(s) PTO/SB/02A attached hereto												